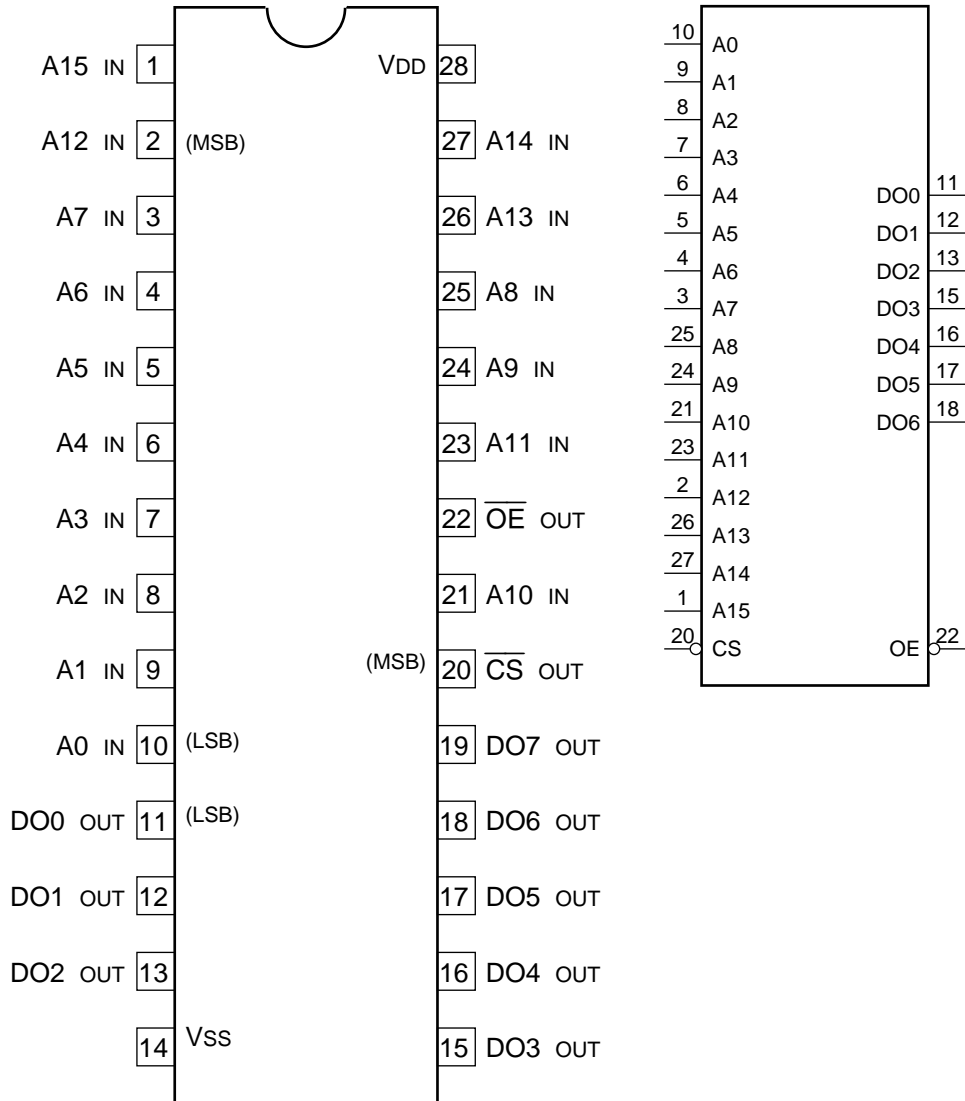


C-MOS 512 K (65,536 × 8) UV-EPROM

—TOP VIEW—



INPUT		DO	OPERATION
\overline{CS}	\overline{OE}		
1	x	HIGH-Z	STAND BY
0	1	HIGH-Z	OPERATING
0	0	DO	OPERATING

0 : LOW LEVEL

1 : HIGH LEVEL

x : LOW OR HIGH LEVEL

INPUT

A0 - A15 : ADDRESS INPUTS

$\overline{\text{CS}}$: CHIP SELECT

OUTPUT

DO0 - DO7 : DATA OUTPUTS

$\overline{\text{OE}}$: OUTPUT ENABLE

